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DOCKET NO.: 4284

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
IN THE MATTER OF THE APPLICATION FOR PATENT

OF: Matthias EICHIN et al.

| Art Unit: 2829

USSN: 10/021,746

| Confirmation No.: 6354

FILED: December 11, 2001

| Examiner: J. M. Hollington

FOR: Method for Testing an Integrated
Circuit

MS NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

June 17, 2003

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ATTN.: DRAWING REVIEW BRANCH

JUN 17 2003

Dear Sir:

TECHNOLOGY CENTER 2800

Applicants are enclosing a file copy of Figs. 1 and 2 in which corrections have been marked in bold. A proposed new Fig. 3, which is to be added to the application, is also enclosed. The corrections in Figs. 1 and 2 merely bring the use of reference numbers of the drawing into conformance with the written description. Approval of the proposed corrections and the entry of new Fig. 3 are respectfully requested. Once the corrections and new Fig. 3 are approved, proper formal drawings will then be filed.

Respectfully submitted,

Matthias EICHIN et al.
Applicant

WFF:ar/4284
Enclosure:
hand-marked file copy
of Figs. 1 and 2, new Fig. 3

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CERTIFICATE OF FAX TRANSMISSION:

I hereby certify that this correspondence with all indicated enclosures is being transmitted by telefax to (703) 872-9318 on the date indicated below, and is addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.

Walter F. Fasse 6/17/03
Name: Walter F. Fasse - Date: June 17, 2003

4284/WFF:ar